

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4070	(ic (integrat\$3 near3 circuit\$1) processor \$1 cpu microprocessor\$1 controller\$1) with (each different\$3 separat\$3 another\$1 other\$1 many) with ((secret\$2 near3 information\$1) (secret \$2 near3 data\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L2	360154	(location\$1 area\$1 address\$3 module\$1 pin\$1 slot\$1) near6 (memor\$3 storage\$1 rom ram eeprom eprom)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L3	75394	(ic (integrat\$3 near3 circuit\$1)) with (processor\$1 cpu microprocessor\$1 controller\$1)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L4	13181	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L5	1646	L4 and L3	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L6	414	L5 and (process\$2 near4 (same\$1 identical\$2 mirror\$1))	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L7	69	L6 and L1	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00

L8	42054	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near6 ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L9	68266	L3 and process\$2	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L10	15078	((713/152) or (713/189) or (713/190) or (713/192) or (713/193) or (713/375) or (713/400) or (726/34) or (711/1) or (711/5) or (711/100) or (711/101-105) or (711/111) or (711/147) or (711/148) or (711/150) or (711/152) or (71/209) or (380/201) or (380/228) or (380/240) or (380/255)).CCLS.	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L11	2088	L10 and L9	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L12	548	L11 and L8	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L13	23419	(ic chip\$1 (integrat\$3 near3 circuit\$1)) near8 (location\$1 area\$1 address\$3 module\$1 pin\$1 slot \$1) near4 (memor\$3 storage\$1 rom ram eeprom eeprom)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00

L14	35	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L15	9	L12 and L14	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L16	160	L13 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1) with (each different\$3 separat\$3 another\$1 other\$1 many)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L17	50	L9 and L16	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L18	50	(ic (integrat\$3 near3 circuit\$1)) and L17	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L19	26	L18 and ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L20	1577	L4 and L9	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L21	54029	L2 with ((secret\$2 near3 information\$1) (secret\$2 near3 data \$1) key\$1 password \$1 encrypt\$4 cipher \$9 crypto\$9 pin code \$1)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L22	6026	L9 and L13	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L23	2220	L8 and L22	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L24	214	L12 and L13	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00

L25	26	L24 and ((tamper\$3 obscur\$4 fault\$1 unsafe malicious\$2 insecure \$4 secur\$6 unsecur \$6) near5 (detect\$4 monitor\$3 scan\$4))	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L26	3943	(tamper\$3 obscur\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecur\$4 secur\$6 unsecur\$6) near5 (detect\$4 monitor\$3 scan\$4) near8 (memor\$4 rom ram eprom eprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L27	13	L24 and L26	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L28	415	(ic chip\$1 (integrat\$3 near3 circuit\$1)) with (tamper detect\$4 obscur\$9 intrusi\$4 intrud\$4) near4 (memor\$3 storage\$1 rom ram eeprom eprom nonvolatile\$1 non-volatile\$1) with (process\$3 microprocess\$3 micro-processor\$1 cpu)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L29	195885	(process\$2 execut\$4 run\$4 code\$1 instruc \$4) near7 (same\$1 identical\$2 mirror\$1) near6 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L30	149604	(process\$2 execut\$4 run\$4 code\$1 instruc \$4) near4 (same\$1 identical\$2 mirror\$1) near4 (software\$1 program\$3 process \$2)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L31	122	L28 and L30	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00

L32	13994	L2 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L33	498	L13 with ((secret\$2 near3 information\$1) (secret\$2 near2 data \$1) (secur\$7 near2 data\$1) (secur\$7 near2 information\$1) (secret\$2 near2 content\$1) (secur\$7 near2 content\$1) key \$1 password\$1 encrypt\$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L34	2	L31 and L33	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L35	122	(ic chip\$1 (integrat\$3 near3 circuit\$1)) with (tamper detect\$4 obscur\$9 intrusi\$4 intrud\$4) near4 (memor\$3 storage\$1 rom ram eeprom eeprom nonvolatile\$1 non-volatile\$1) with ((secret\$2 near3 information\$1) (secret \$2 near2 data\$1) (secur\$7 near2 data \$1) (secur\$7 near2 information\$1) (secret \$2 near2 content\$1) (secur\$7 near2 content\$1) key\$1 password\$1 encrypt \$4 cipher\$9 crypto\$9 (secret\$2 near2 (pin code\$1)))	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L36	74	L31 and L35	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00

L37	4475	(tamper\$3 obscure\$4 fault\$1 fraud\$7 unsafe malicious\$2 insecure\$4 secur\$6 unsecur\$6) near8 (detect\$4 monitor\$3 scan\$4) near8 (memor\$4 rom ram eeprom eeprom storage\$1)	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L38	74	L36 and L37	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L39	74	L38 and tamper\$4	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L40	388	simon near3 walmsley	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L41	120	L40 and L3	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L42	15	L41 and L37	USPAT; IBM_TDB	OR	OFF	2009/11/17 17:00
L43	2	(integrate\$1 and circuit\$1 and tamper and line and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2009/11/17 17:00
L44	1	(integrate\$1 and circuit\$1 and location \$1 and non-volatile and plurality and secret\$1 and memory and processor\$1 and chip\$1).dm.	US-PGPUB	OR	ON	2009/11/17 17:00
L45	190	tamper\$3 near5 detect\$4 near3 line\$1	US-PGPUB	OR	ON	2009/11/17 17:00
L46	172	L45 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2009/11/17 17:00
L47	158	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2009/11/17 17:00
L48	472	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	US-PGPUB	OR	ON	2009/11/17 17:00

L49	304	L48 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2009/11/17 17:00
L50	146	L49 not L47	US-PGPUB	OR	ON	2009/11/17 17:00
L51	210	tamper\$3 near5 detect\$4 near3 line\$1	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L52	136	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L53	366	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L54	362	L53 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L55	136	L52 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L56	362	L54 not L47	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L57	162	L56 and (obscure\$4 obfuscate\$4)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L58	380	(obscure\$4 obfuscate \$4) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L59	2	L56 and L58	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L60	0	((tamper\$3 intruder\$4 intruder\$3) near5 detect \$4 near3 (circuit\$3 line \$1)) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/11/17 17:00

L61	201	((tamper\$3 intrud\$4 intrud\$3) near5 detect \$4 near3 (circuit\$3 line\$1)) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L62	200	L61 not L47	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L63	5496	(obscure\$4 obfuscate \$4) near5 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L64	6317	(obscure\$4 obfuscate \$4) near8 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L65	1	L62 and L64	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L66	63112	(decrypt\$9 decipher \$9 decode\$3 obfuscate \$3 obscure\$9) near6 (operation\$1 attempt \$4 access\$3)	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L67	136	L62 and L66	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L68	120	L67 and (ic (integrate \$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L69	111	L68 and (flash near3 (storage\$1 memory \$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L70	3256	(flash near3 (storage \$1 memory\$1)) near8 (ic (integrate\$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L71	4	L69 and L70	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L72	160	(flash near3 (storage \$1 memory\$1)) near4 ((different\$2 various) near4 (location\$1 area\$1))	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L73	160	L72 not L71	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00
L74	22	L72 and L70	USPAT; EPO; IBM_TDB	OR	ON	2009/11/17 17:00

EAST Search History (Interference)

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